

FORM PTO-1449	Atty Docket	Serial No.
INFORMATION DISCLOSURE CITATION	RAL9-2000-0069-US1	To be assigned
	Applicant Paul D. Bryan, et al.	
	Filing Date ***	Group Art Unit Unassigned

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
<i>KSN</i>	AA	4,958,347	09/18/90	White et al.	371 714	29.543	PTO
<i>KSN</i>	AB	5,724,504	03/03/98	Aharon et al.	395 714	183.9	33
<i>KSN</i>	AC	5,815,688	09/29/98	Averill	395 714	500	4
<i>KSN</i>	AD	5,898,705	04/27/99	Graef	371 714	27.1	738
<i>KSN</i>	AE	5,930,482	07/27/99	Carter et al.	395 714	280	100
<i>KSN</i>	AF	5,938,777	08/17/99	Carter	714	32	
<i>KSN</i>	AG	5,941,971	08/24/99	Carter	710 714	129.3	11
<i>KSN</i>	AH	5,978,934	11/02/99	Gates	714	41	
<i>KSN</i>	AI	5,996,034	11/30/99	Carter	710	100	
<i>KSN</i>	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
<i>KSN</i>	AL	10340251A	12/22/98	JP	G06F	13/362	Yes No
	AM						Yes No
	AN						Yes No
	AO						Yes No
	AP						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>KSN</i>	AR	IBM Technical Disclosure Bulletin, vol. 24, No. 7A, December 1981, pp3282-3283
	AS	"Dynamic Processor Bus LSSD Simulation Modeling Technique"
	AT	

Examiner <i>KSN</i>	Date Considered 11/26/03
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609.

Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to Applicant.